

3 image
MAR 16 2004
U.S. TRADEMARK OFFICE
SEARCHED SERIALIZED INDEXED
S-1186

3 image 11/12823

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

JOZEF D. MITROS ET AL.

Serial No. 10/020,034 (TI-32931)

Filed December 7, 2001

For: METHOD FOR FABRICATING LOW CHC
DEGRADATION MOSFET TRANSISTORS

Art Unit 2823

Examiner Long Pham

Customer No. 23494

Director of the United States
Patent and Trademark Office
P. O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING OR TRANSMISSION UNDER 37 CFR 1.8

I hereby certify that the attached document is being deposited with the United States Postal Service with sufficient postage for First Class Mail in an envelope addressed to Director of the United States Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450 or is being facsimile transmitted on the date indicated below:

3-15-04

Jay M. Cantor, Reg. No. 19,906

BRIEF ON APPEAL

REAL PARTY IN INTEREST

The real party in interest is Texas Instruments Incorporated, a Delaware corporation with offices at 7839 Churchill Way, Dallas, Texas 75251.

RELATED APPEALS AND INTERFERENCES

There are no known related appeals and/or interferences.

STATUS OF CLAIMS

This is an appeal of claims 18, 19, 26 and 27, all of the rejected claims. Claims 1 to 17, 20 to 25 and 28 to 30 have been allowed. Please charge any costs to Deposit Account No. 20-0668.

03/19/2004 PWALKER 00000001 200668 10020034

01 FC:1402

330.00 DA

TI-32931-1